

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

re Patent Application of

Hajime KIMURA

Serial No. 10/004,483

Filed: December 6, 2001

For: SEMICONDUCTOR DEVICE

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) Group Art Unit: 2826

) Examiner: F. Abraham

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CERTIFICATE OF MAILING) I hereby certify that this correspondence is being deposited with
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Class Mail in an envelope addressed to: Commissioner for Patents,
Washington, D.C. 20231, on February 13, 2003.

Adele M. Stamper

AMENDMENT AND RESPONSE TO ELECTION REQUIREMENT

Honorable Commissioner for Patents

Washington, D.C. 20231

Sir:

In response to the Office Action of January 13, 2003, please amend the subject application as follows:

IN THE CLAIMS:

Cancel claims 2-4, 7, 8, 12, 14-16, 19, 20, 24, 26-28, 31, 32, 36, 38-40, 43, 44, 48, 50-52, 55, 56, 60, 62, 66, 70, 74, 78, 82, 86, 87, 90, 91, 94, 95, 96, 99, 102, 103, 106, 107, 110-112, 115, 116, 120, 122-124, 127, 128, 132, 134, 136, 138, 141, 143, 145, 148-150, 152, 155, 157-159, 162, 163, 167, 169, 171-173, 176, 177, 181, 183, 185-187, 190, 191, 195, 197, 199-201, 204, 205, 209, 211-233 without prejudice to applicants' right to file a divisional application with respect thereto.

Please amend claims 5 and 6 as follows:

5. (Amended) A semiconductor device comprising:
 - a pixel portion comprising a plurality of pixels;
 - a signal line driver circuit; and
 - an output switching circuit